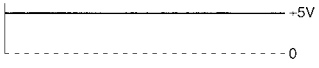

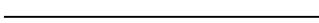









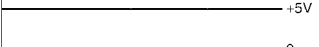




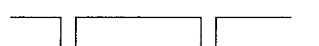


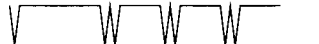


Comp	Name (pin) Circ /IC	Scope	Freq. Per.	Data H/L/A	Description
40	FRONTDATA2/HS1.1		0	H	Frontdata2 /High speed input 1.1
41	/HS0.4		400 Hz	A	/High Speed input 0.4
42	/VSS		0	L	/Voltage supply ground (0)
43	AD15 /HS0.5		30-60 ms	L/A	Address data 15/High Speed input 0.5
44	LIGHT/HS0.0		0	L	Light /High Speed input 0.0
45	/HS0.1		0	L	/High Speed input 0.1
46	PS1 /P1.5		0	L	page select 1 /port 1.5
47	PS2 /P1.6		45-1.5 kHz	A	page select 2 /port 1.6
48	PS3 /P1.7		45-1.5 kHz	A	page select 3 /port 1.7
49	DTAEC/P2.6		0	L	Data enable C /port 2.6
50	/HS0.2		0	L	/High Speed input 0.2
51	/VSS		0	L	/Voltage supply ground (0)
52	/VCC		0	H	/Main supply (+5V)
53	ON OFF/HS0.3		0	L	on off /High Speed input 0.2
54	/VSS		0	L	/Voltage supply ground (0)
55	/VSS		0	L	/Voltage supply ground (0)
56	/VPP		0	H	/(+5V)
57	FRONT LATCH /T2CAP/P2		≈30 ms	H/A	Front latch /
58	TEST2/PWM		12 kHz	A	Test 2 /Pulse width modulator
59	WRIN /WRL/WR		2.08 MHz	A	Write not /Write low/Write
60	/WRH/BHE		2.08 MHz	A	/Write high, Bus High Enable